

• General Description

The CH50N100BD combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is ideal for load switch and battery protection applications.

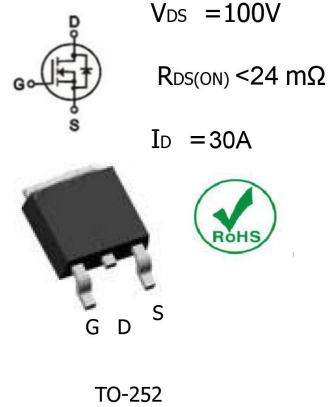
• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

• Product Summary



• Ordering Information:

Part NO.	CH50N100BD
Marking	CH50N100BD
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2500

• Absolute Maximum Ratings ($T_c = 25^\circ C$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_{D@TC=25^\circ C}$	30	A
	$I_{D@TC=75^\circ C}$	25	A
	$I_{D@TC=100^\circ C}$	18	A
Pulsed Drain Current	I_{DM}	120	A
Total Power Dissipation($TC=25^\circ C$)	$P_{D@TC=25^\circ C}$	41	W
Total Power Dissipation($TA=100^\circ C$)	$P_{D@TC=100^\circ C}$	30	W
Operating Junction Temperature	T_J	-55 to 150	$^\circ C$
Storage Temperature	T_{STG}	-55 to 150	$^\circ C$
Single Pulse Avalanche Energy@ $L=0.1mH$	E_{AS}	30	mJ
Avalanche Current@ $L=0.1mH$	I_{AS}	30	A

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	3.0		$^{\circ}\text{C/W}$
Thermal resistance, junction - ambient	R_{thJA}	-	-	62.5	$^{\circ}\text{C/W}$
Soldering temperature, wavesoldering for 10s	T_{sold}	-	-	125	$^{\circ}\text{C}$

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.8	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=10A$		20	26	m Ω
		$V_{GS}=4.5V, I_D=10A$		25	31	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=15V, I_D=10A$		18		S
Source-drain voltage	V_{SD}	$I_S=8A$			1.20	V

•Electronic Characteristics

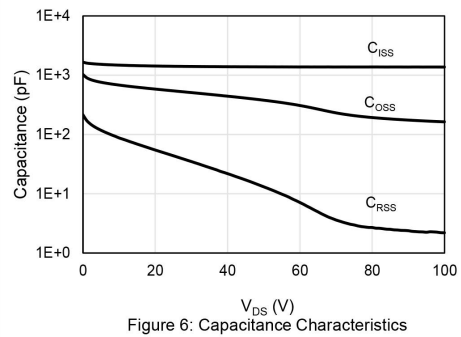
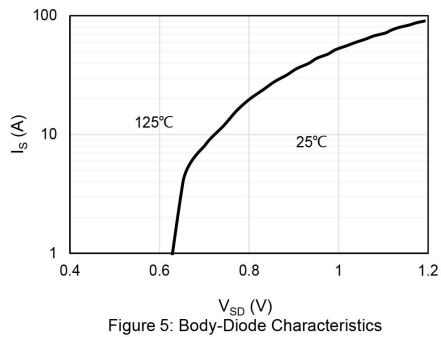
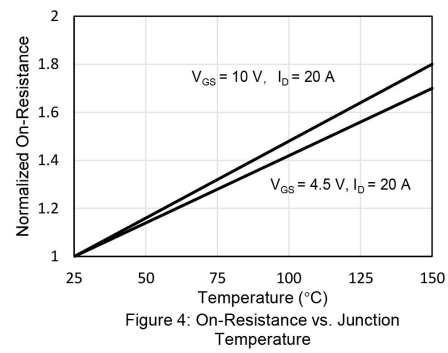
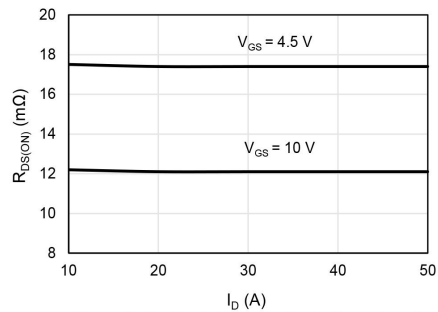
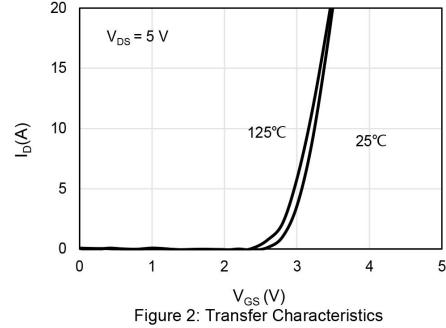
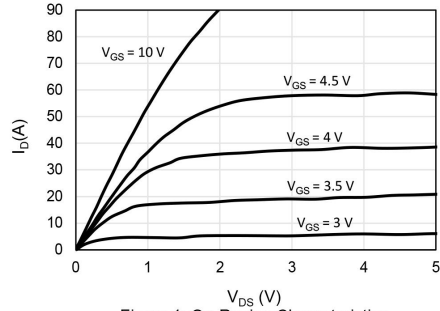
Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C_{iss}	$f = 1\text{MHz}$ $V_{DS}=25V$	-	660	-	pF
Output capacitance	C_{oss}		-	375	-	
Reverse transfer capacitance	C_{rss}		-	21	-	

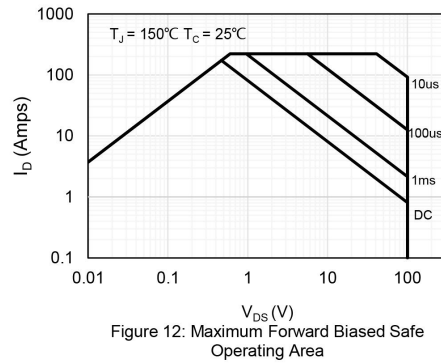
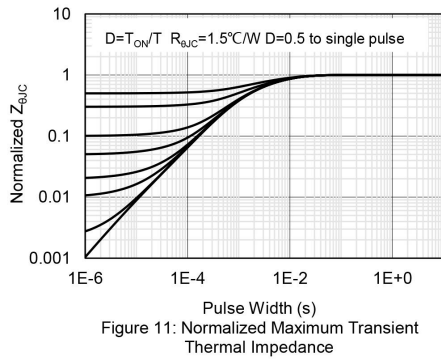
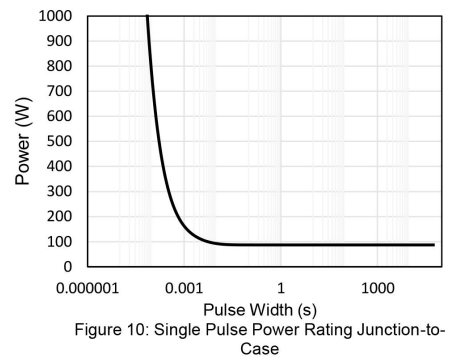
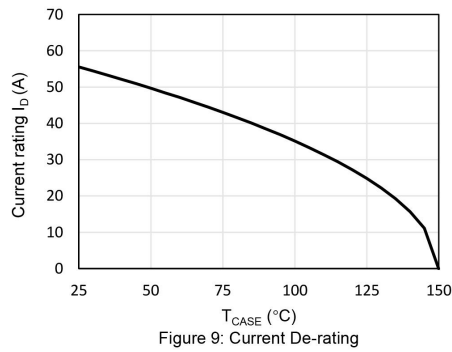
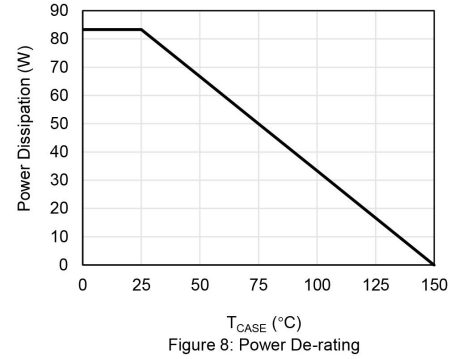
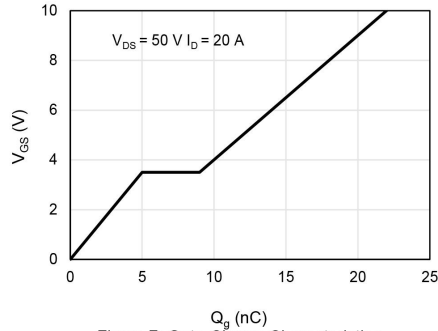
•Gate Charge characteristics($T_a = 25^{\circ}\text{C}$)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q_g	$V_{DS}=50V$	-	25	-	nC
Gate - Source charge	Q_{gs}	$I_D=10A$	-	6.0	-	
Gate - Drain charge	Q_{gd}	$V_{GS}=10V$	-	5.0	-	

Note: ① Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$;

Typical Performance Characteristics





Test Circuit and Waveform

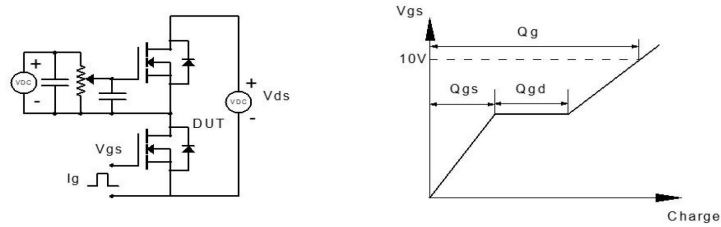


Figure 1: Gate Charge Test Circuit & Waveform

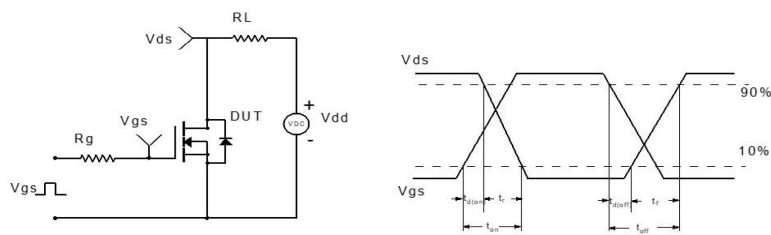


Figure 2: Resistive Switching Test Circuit & Waveform

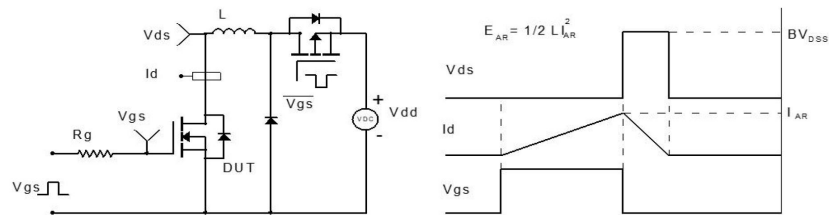


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

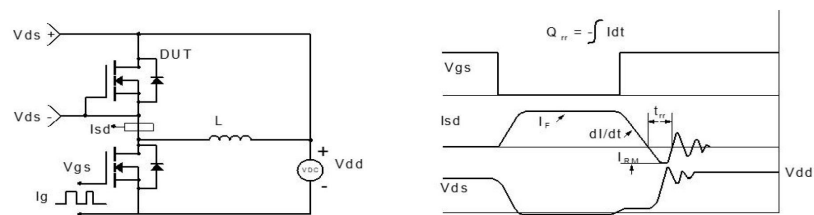
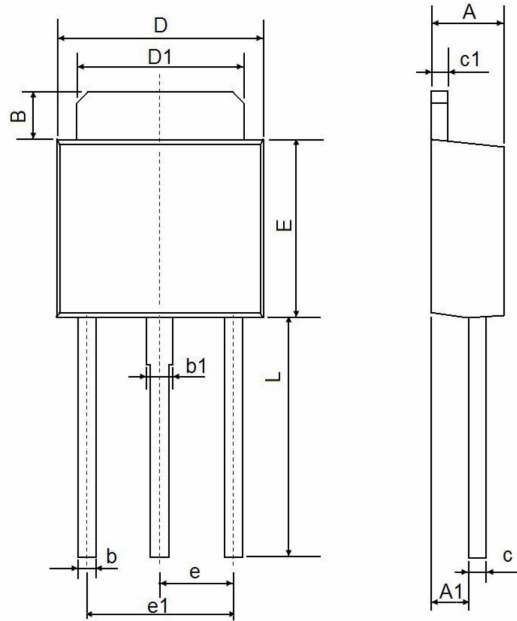


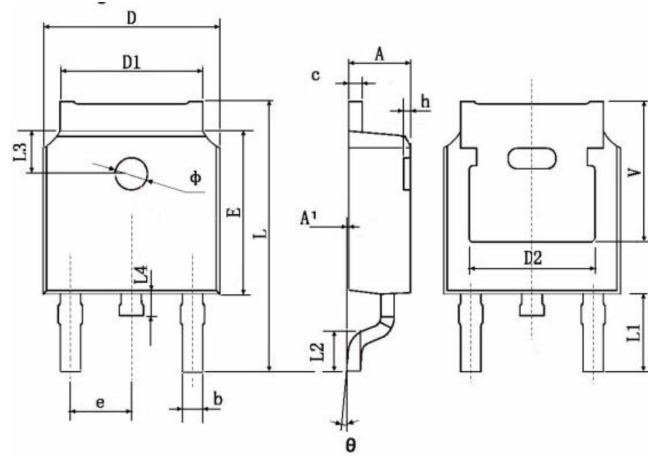
Figure 4: Diode Recovery Test Circuit & Waveform

TO-251 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	1.050	1.350	0.042	0.054
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	7.500	7.900	0.295	0.311

Package Outlines



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.250	2.350	0.089	0.093
A1	0.050	0.150	0.002	0.006
b	0.660	0.860	0.026	0.034
c	0.458	0.558	0.018	0.022
D	6.550	6.650	0.259	0.263
D1	5.234	5.434	0.207	0.215
D2	4.826 TYP.		0.191 TYP.	
E	6.050	6.150	0.239	0.243
e	2.236	2.336	0.088	0.092
L	9.820	10.220	0.388	0.404
L1	3.000 TYP.		0.119 TYP.	
L2	1.400	1.600	0.055	0.063
L3	1.800 TYP.		0.071 TYP.	
L4	0.700	0.900	0.028	0.036
Φ	1.150	1.250	0.045	0.049
θ	0°	3°	0°	3°
h	0.000	0.300	0.000	0.012
V	5.399 TYP		0.213 TYP	