

• General Description

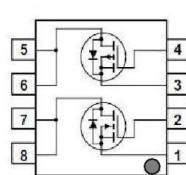
It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. It combines one N channel MOSFET and one P channel MOSFET.

• Features

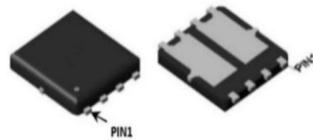
- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Dual DIE in one package

• Application

- Power Management in Notebook Computer
- BLDC Motor driver

• Product Summary


$V_{DS1} = 60V$
 $V_{DS2} = -60V$
 $R_{DS(ON)1} = 35m\Omega$
 $R_{DS(ON)2} = 28m\Omega$
 $I_D1 = 20A$
 $I_D2 = -10A$



DFN3*3

• Ordering Information:

Part NO.	CH20NP06SN
Marking	CH20NP06SN
Packing Information	REEL TAPE
Basic ordering unit (pcs)	5000

• Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	3.3	$^{\circ}\text{C/W}$
Thermal resistance, junction - ambient	R_{thJA}	-	-	62	$^{\circ}\text{C/W}$
Soldering temperature, wavesoldering for 10s	T_{sold}	-	-	265	$^{\circ}\text{C}$

N Channel Absolute Maximum Ratings ($T_c = 25^{\circ}\text{C}$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D @ T_c = 25^{\circ}\text{C}$	20	A
	$I_D @ T_c = 75^{\circ}\text{C}$	16	A
	$I_D @ T_c = 100^{\circ}\text{C}$	14	A

Pulsed Drain Current ^①	I _{DM}	60	A
Total Power Dissipation	P _D @T _C =25°C	25	W
Total Power Dissipation	P _D @T _A =25°C	2.0	W
Operating Junction Temperature	T _J	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C
Single Pulse Avalanche Energy	E _{AS}	72	mJ

• P Channel Absolute Maximum Ratings (T_C = 25°C)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D @T _C = 25°C	-10	A
Continuous Drain Current	I _D @T _C =100°C	-6.3	A
Pulsed Drain Current ^①	I _{DM}	-32	A
Total Power Dissipation	P _D @T _A =25°C	3.0	W
Operating Junction Temperature	T _J	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C
Single Pulse Avalanche Energy	E _{AS}	60	mJ

• N Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250μA	60			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250μA	1.2	1.6	2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V			1.0	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 20A		24	35	mΩ
		V _{GS} = 4.5V, I _D = 10A		30	40	mΩ
Forward Transconductance	g _{FS}	V _{DS} = 25V, I _D = 30A	11			s
Source-drain voltage	V _{SD}	I _S = 20A			1.20	V

• Dynamic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C _{iss}	f = 1MHz	-	590	-	pF
Output capacitance	C _{oss}		-	70	-	

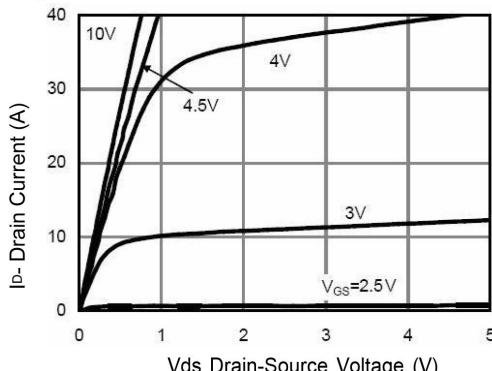
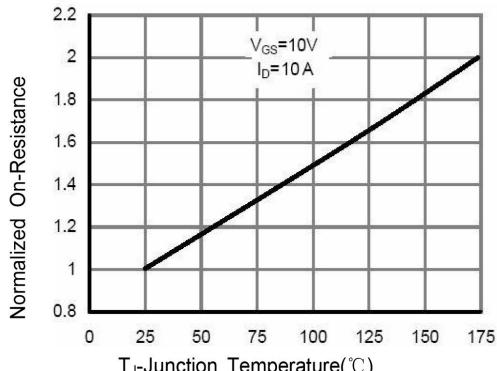
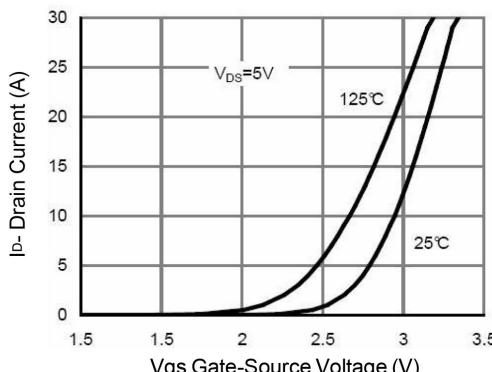
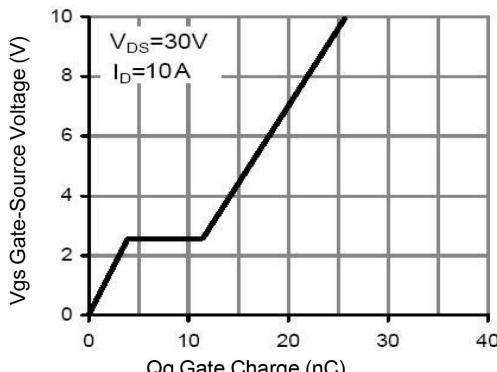
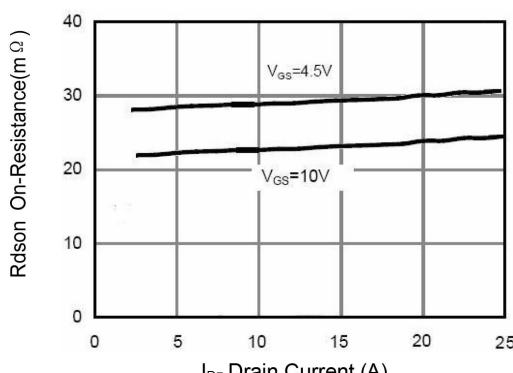
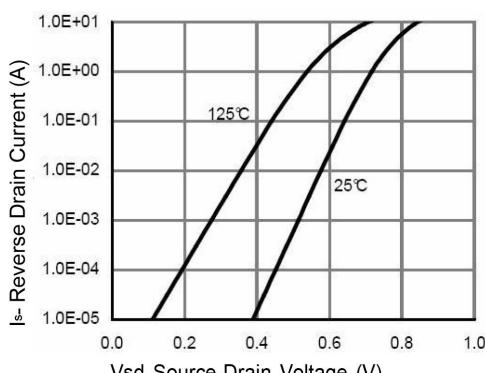
Reverse transfer capacitance	Crss	V _{DS} =25V	-	64	-	
Total gate charge	Qg	V _{DD} = 25V I _D = 5A V _{GS} = 10V	-	25.3	-	nC
Gate - Source charge	Qgs		-	4.7	-	
Gate - Drain charge	Qgd		-	6.1	-	

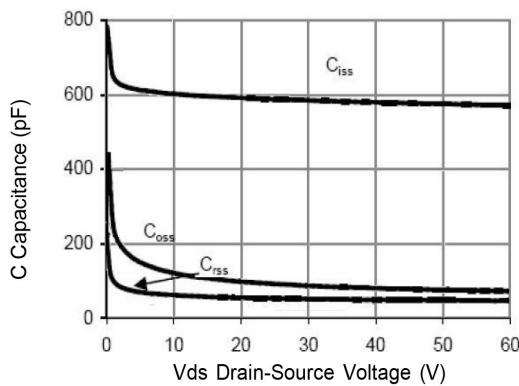
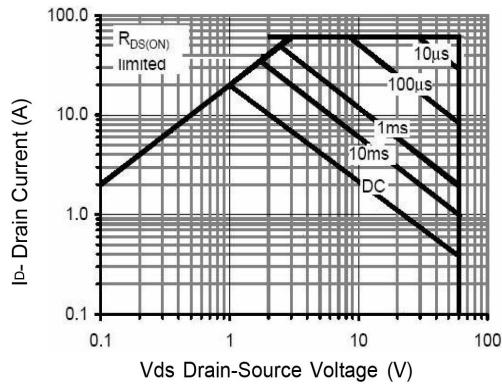
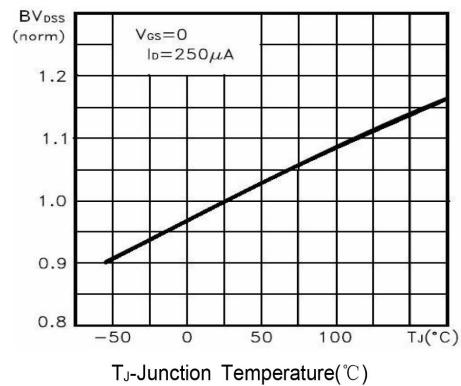
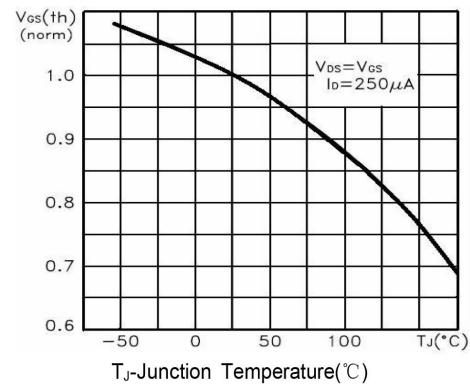
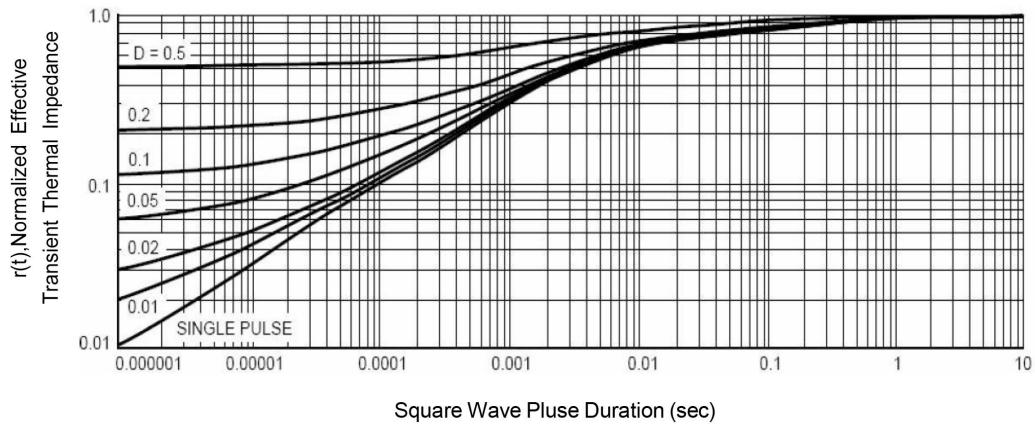
• P Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-60			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =-250uA	-1.3	-1.8	-2.3	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Static Drain-source On Resistance	R _{DSS(ON)}		60	76		mΩ
Forward Transconductance	g _{FS}	V _{DS} =-10V, I _D =-5A		7		s

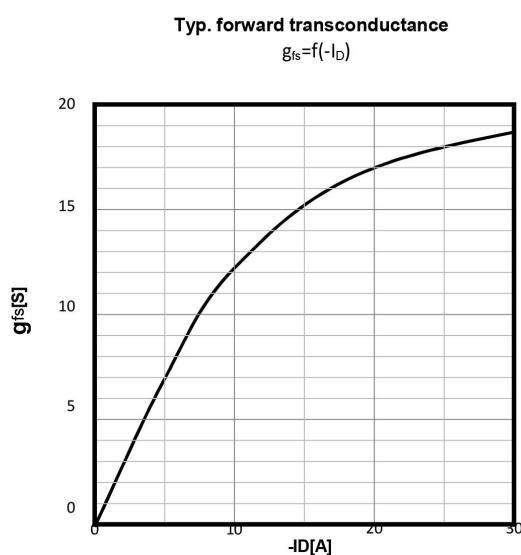
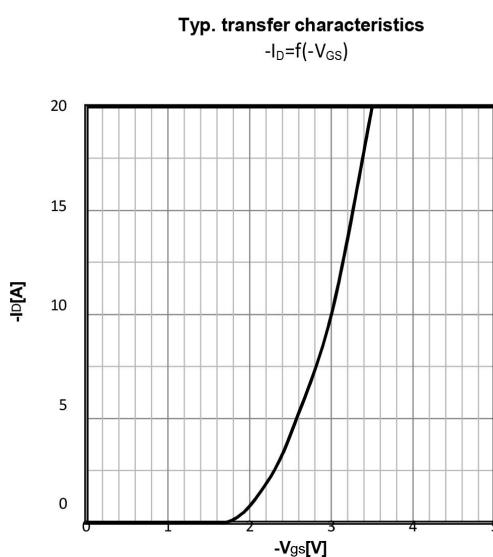
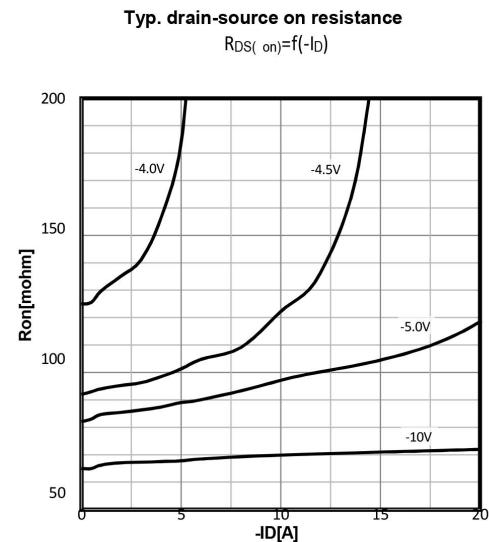
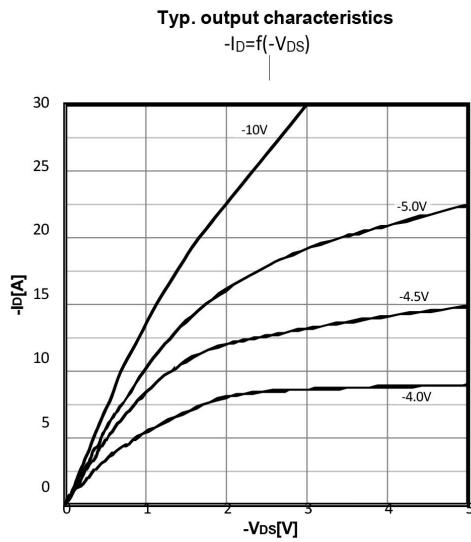
• Dynamic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C _{iss}	f = 1MHz V _{DS} =-25V	-	525	-	pF
Output capacitance	C _{oss}		-	80	-	
Reverse transfer capacitance	Crss		-	3.9	-	
Total gate charge	Qg	V _{DD} = -15V I _D = -4A V _{GS} = -10V	-	8.5	-	nC
Gate - Source charge	Qgs		-	1.8	-	
Gate - Drain charge	Qgd		-	1.5	-	

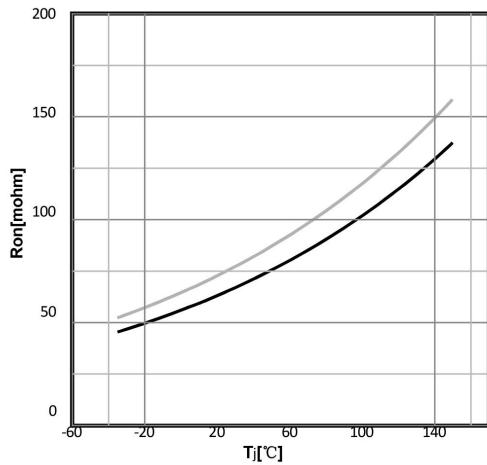
Typical Electrical and Thermal Characteristics (Curves)

Figure 1 Output Characteristics

Figure 4 Rdson-Junction Temperature

Figure 2 Transfer Characteristics

Figure 5 Gate Charge

Figure 3 Rdson-Drain Current

Figure 6 Source-Drain Diode Forward


Figure 7 Capacitance vs Vds

Figure 8 Safe Operation Area

Figure 9 BV_{dss} vs Junction Temperature

Figure 10 $V_{GS(\text{th})}$ vs Junction Temperature

Figure 11 Normalized Maximum Transient Thermal Impedance

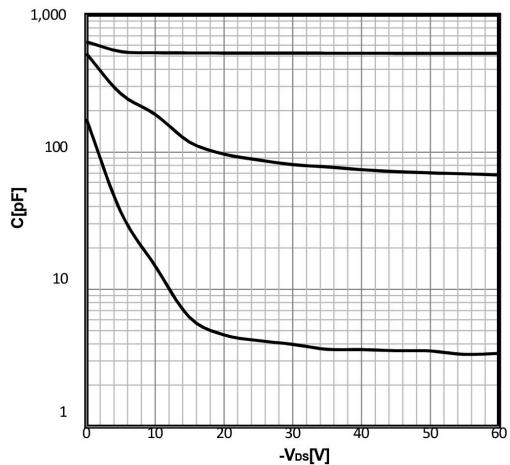
• P Channel characteristics curve



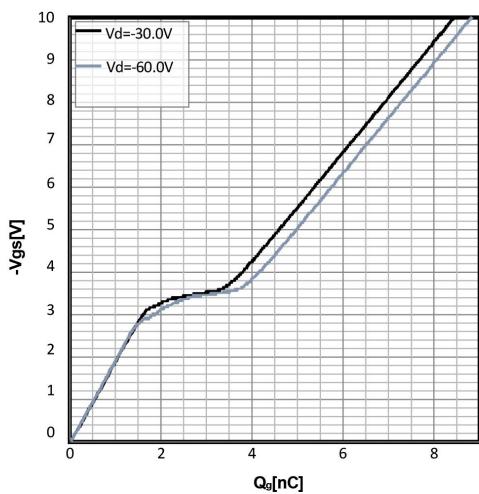
Drain-source on-state resistance
 $R_{DS(on)} = f(T_j)$; $I_D = -5.0A$; $V_{GS} = -10V$



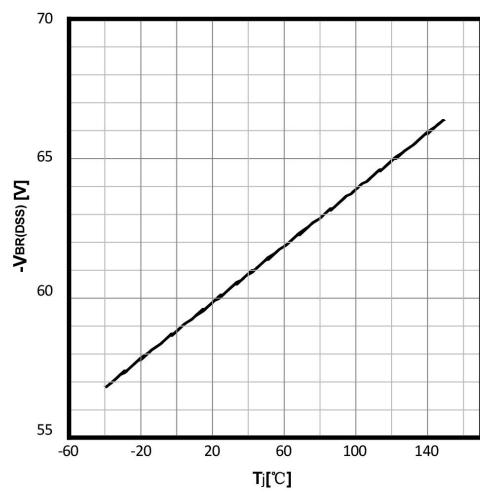
Typ. capacitances
 $C = f(-V_{DS})$; $V_{GS} = 0V$; $f = 1MHz$

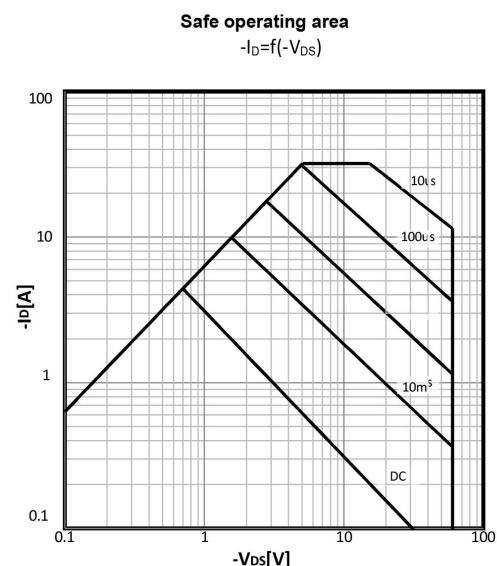
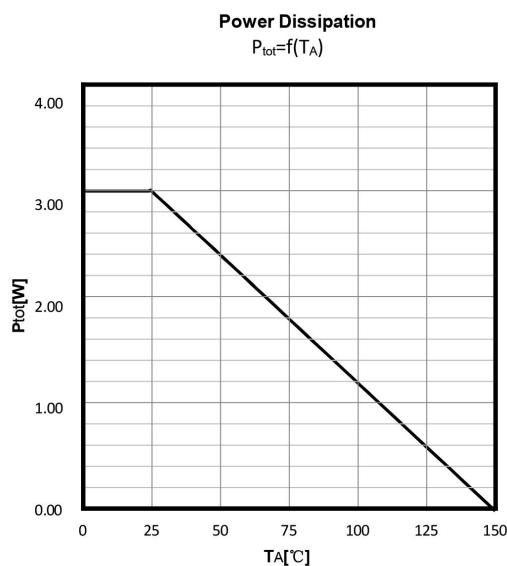


Typ. gate charge
 $-V_{GS} = f(Q_{gate})$; $I_D = -5A$



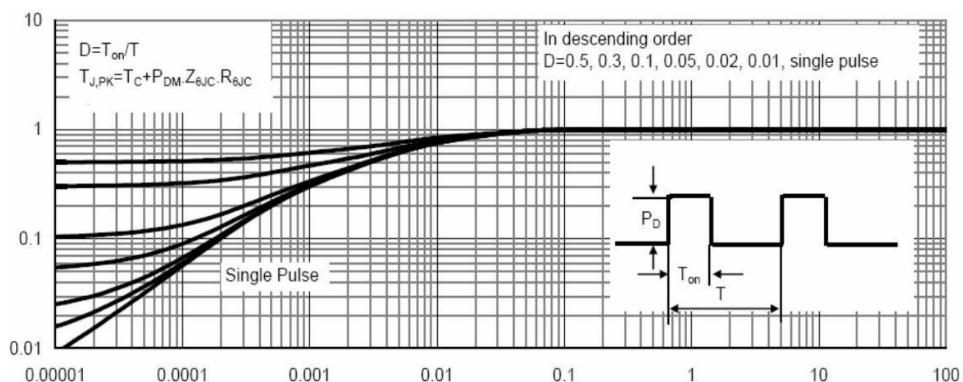
Drain-source breakdown voltage
 $-V_{BR(DSS)} = f(T_j)$; $I_D = -250\mu A$

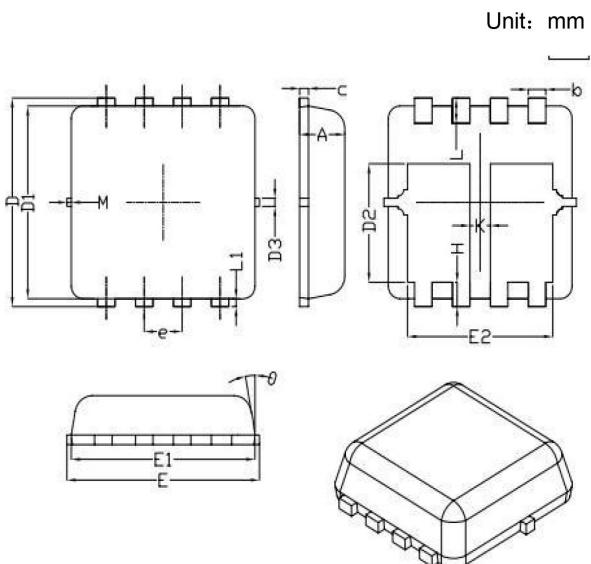




Max. transient thermal impedance

$Z_{thJC}=f(t_p)$



• Dimensions (DFN3x3)


SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
K	0.30	---	---
θ	---	10°	12°
M	*	*	0.15

** Not specified*